



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

JFW  
RB

Applicant:

See Taur Lee, et al.

Serial No.: 10/689,311

Filed: 10/20/2003

For: **Programmable Linear-In-DB Or Linear Bias Current Source and Methods to Implement Current Reduction In A PA Driver With Built-In Current Steering VGA**

Docket: TI-36130

Art Unit: 2816

Examiner: Cunningham, Terry

Not. of All.: 07/28/2005

Conf. No.: 2873

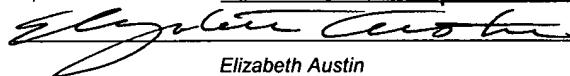
LETTER OF TRANSMITTAL

Commissioner for Patents  
Alexandria, VA 22313-1450

Sir:

*Mailing Certificate Under 37 C.F.R. § 1.8(A)*

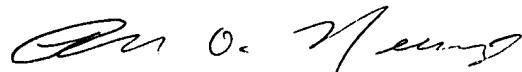
I hereby certify that, on the date indicated below, this correspondence is being deposited with the United States Postal Service as First Class mail in an envelope addressed to: Commissioner for Patents and Trademarks, Alexandria, VA 22313-1450 on Oct. 17, 2005.

  
Elizabeth Austin

The Supplemental Declarations in the above identified application is/are enclosed.

Please charge any necessary fee to the deposit account of Texas Instruments Incorporated, Account No. 20-0668. An original and two copies of this sheet are enclosed.

Respectfully submitted,



Ronald O. Neerings

Attorney for Applicants

Registration No. 34,227

Texas Instruments, Incorporated  
P.O. Box 655474, M/S 3999  
Dallas, Texas 75265  
(972) 917-5299